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National Institute of Technology, Hamirpur (HP)

Name of Examination: B.Tech. End Semester Examination Nov./ Dec. 2022

Branch: ECE/ ECE (Dual-Degree)

Semester: 3rd

Subject: Analog Electronics

Subject Code: EC-212

Time: 3 Hours

Maximum Marks: 50

Note: Attempt all questions

- Q. 1(a) Using common emitter transistor amplifier circuit and giving ac output current-voltage waveforms, discuss and indicate the cut-off, saturation and active regions over the transistor output characteristics.
 - (b) How Miller capacitance effect accounts for an increase in the equivalent (5) input capacitance of an inverting voltage amplifier?
 - Q. 2 Four identical cascaded stages have an overall upper 3 dB frequency of 20 kHz (10) and a lower 3 dB frequency of 20 Hz.
 - (a) Determine f_1 and f_2 of each stage.
 - (b) Find the frequency range over which the voltage gain is down by less than 2 dB from its midband value.
 - Q. 3 Sketch the circuit of a push-pull Class B transistor amplifier in the common-collector configuration (a) with an output transformer, (b) without an output transformer. Explain their operation and in particular show that no even harmonics are present.
- Q. 4(a) Illustrate practical circuits realizing voltage-series and current-shunt feedback. (5) Derive the expressions of voltage gain/ current gain, input resistance, and output resistance.
 - (b) An voltage amplifier with feedback gives a voltage gain equal to 40 V/V. In order to produce a specific output voltage, the input voltage required without feedback is 0.1 V. When feedback has been provided the input must be increased to 2.4 V to produce the same output. Calculate the feedback factor.
- Q. 5(a) Explain Barkhausen criteria for oscillations. Derive the frequency of oscillation for (5) Wien-bridge oscillator.
 - (b) Draw the circuit diagram of an LC tank based oscillator such that resonant (5) frequency remains unaffected from the internal capacitance of the transistor.

****End of Question Paper***